



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Johannes Benedikt et al. Docket Number: 107687.00019
(ABEL-0007)

Serial No.: 10/565,519 Art Unit: Not Yet Assigned

Filed: January 23, 2006 Examiner: Not Yet Assigned

For: HIGH FREQUENCY CIRCUIT
ANALYSER

INFORMATION DISCLOSURE STATEMENT

Mail Stop: AMENDMENT
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Joan B. Parragher

In accordance with the requirements of 37 C.F.R. §§ 1.97 and 1.98, attached please find a Form PTO/SB/08A listing information for consideration by the Office in connection with its examination of the above-captioned patent application. Copies of the foreign patents and articles are enclosed herein.

Applicants submit that no representation is made, and no representation is intended, that more relevant material does not exist, or that the order of presentation of these materials in any way reflects their relative pertinence. The listing on the attached Form PTO/SB/08A is not intended to constitute an admission of any kind. Specifically, this presentation is not an admission that any of the items listed are properly citable against the above-identified application as prior art.

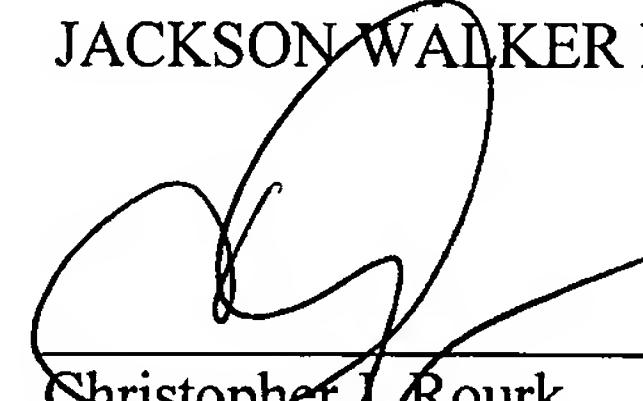
Applicants respectfully submit that their invention is patentable over the enclosed documents listed on Form PTO/SB/08A.

This Information Disclosure Statement is being filed prior to the issuance of the first Office Action.

No fee is believed to be due with the filing of this Information Disclosure Statement. However, the Commissioner of Patent and Trademarks is hereby authorized to charge any deficiency or to credit any fee overpayment relating to this matter to Deposit Account No. 50-1752.

Respectfully submitted,

JACKSON WALKER L.L.P.



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Substitute for Form 1449A/PTO

JUN 16 2006

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet	1	of	1	Attorney Docket No.	107687.00019 (ABEL-0007)
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Complete If Known

Application Number	10/565,519
Filing Date	January 23, 2006
First Named Inventor	Johannes Benedikt
Group Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee of Applicant of Cited Doc.	Date of Pub. Of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
AA	6,479,977			Mordkovich	11/12/2002	
AB	6,509,743			Ferrero	01/21/2003	
AC	6,204,673			Andeen et al.	03/20/2001	
AD	10/565,519					A prior art active load pull circuit as shown in Figure 1 of the present application and described on pg. 1, line 30 to pg. 3, line 5 of the present application.
AE						
AF						
AG						
AH						
AI						
AJ						
AK						

FOREIGN PATENT DOCUMENTS

Exam. Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee of Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ² (if known)				
BA	GB	2382662			Univ. College Cardiff Consultants Ltd.	06/04/2003		
BB	JP	2002318256			Mitsubishi Electric Corp.	10/31/2002		
BC	GB Search Report	0317349.9						
BD	PCT Search Rpt	PCT/US2004 /003245						
BE								
BF								
BG								
BH								

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Exam. Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
CA		FERRERO et al., "Recent Technological Advances for Modular Active Harmonic Load-Pull Measurement Systems," CONFERENCE PROCEEDINGS MILLER FREEMAN LONDON, UK, 5 Oct. 1999, 4 pgs., (XP-002303785)	
CB		WILLIAMS et al., "A Study of the Effect of Envelope Impedance on Intermodulation Asymmetry Using a Two-tone Time Domain Measurement System," IEE MTT S INT MICROWAVE SYMP DIG' IEEE MTT-S INT'L MICROWAVE SYMPOSIUM DIGEST 2002, vol. 3, 2002, 4pgs. (XP-002303884)	
CC		Print-out of part of a website by Focus Microwaves, Inc. (url http://www.focus-microwaves.com/FAQs/FAQ_Links/basics.htm)	
CD		ROTELLA et al., "Harmonic Balance Device Analysis of an LDMOS RF Power Amplifier with Parasitics and Matching Network," PROCEEDINGS OF THE 1999 PARTICLE ACCELERATOR CONFERENCE, New York, 1999, 8 pgs.	
CE		CORREDOURA, "Architecture and Performance of the PEP-II Low-Level RF System, PROCEEDINGS OF THE 1999 PARTICLE ACCELERATOR CONFERENCE, New York, 1999, 5 pgs.	
CF		Agilent PN 89400-8. Using Vector Modulation Analysis in the Integration, Troubleshooting, and Design of Digital RF Communications Systems. Product note. 29 pgs.	
CG		BENEDIKT et al., "High-Power Time-Domain Measurement Bench for Power Amplifier Development," 2002 ARFTG Conference Digest, Washington, D.C., Dec. 5-6, 2002, 4 pgs.	
CH		BENEDIKT et al., "High-Power Time-Domain Measurement Bench for Power Amplifier Development," Powerpoint presentation given at 2002 ARFTG Conference, Washington, D.C., Dec. 5-6, 2002, 30 pgs.	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

US and Foreign Patent Documents: ¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

Other Prior Art/Non-Patent Literature Documents: ¹Unique citation designation number. ²Applicant is to place a check mark here if English Translation is attached.

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